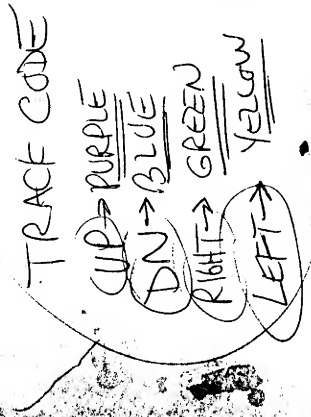
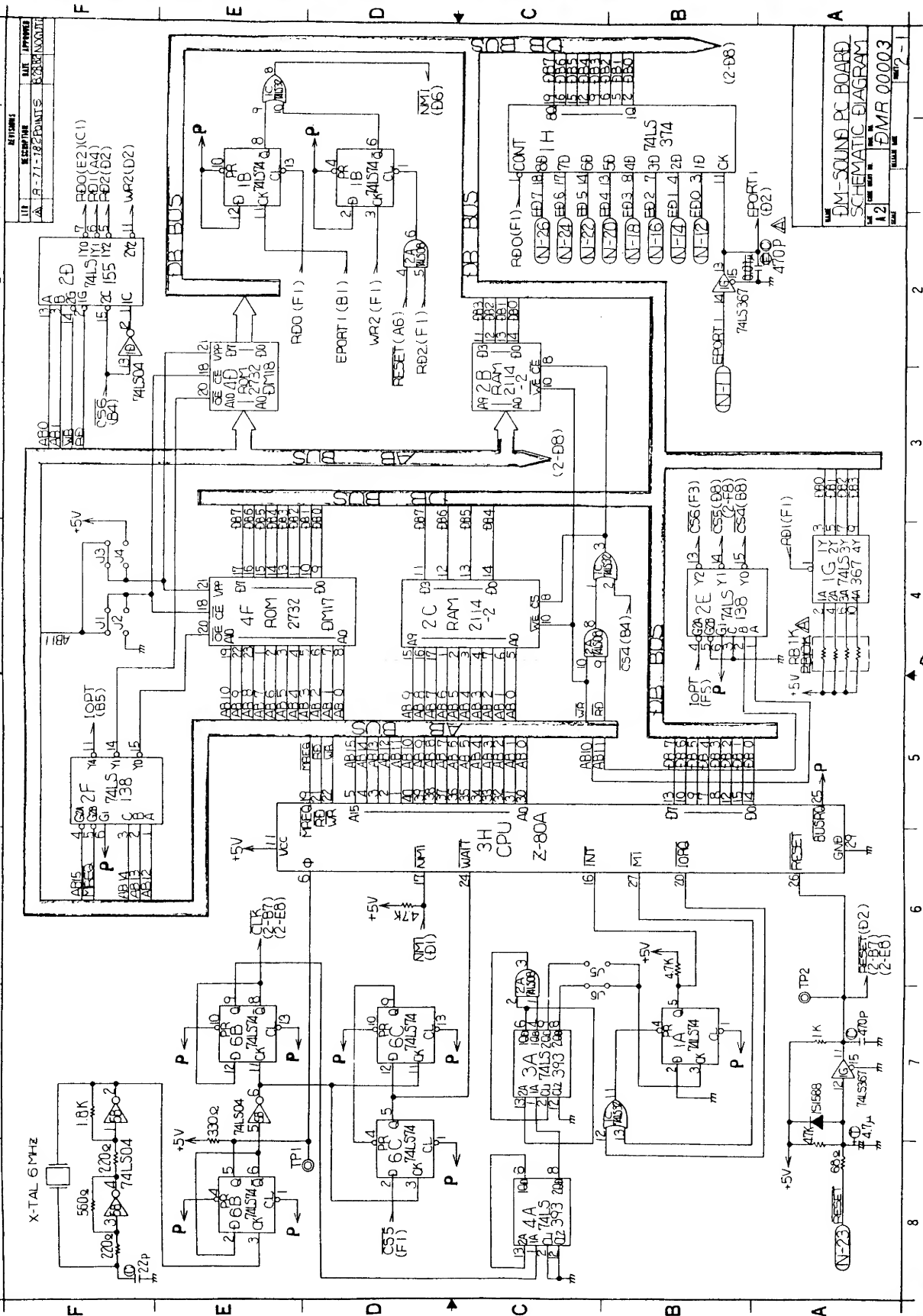


BIRDIE-KING II

Schematics and Wiring Diagrams



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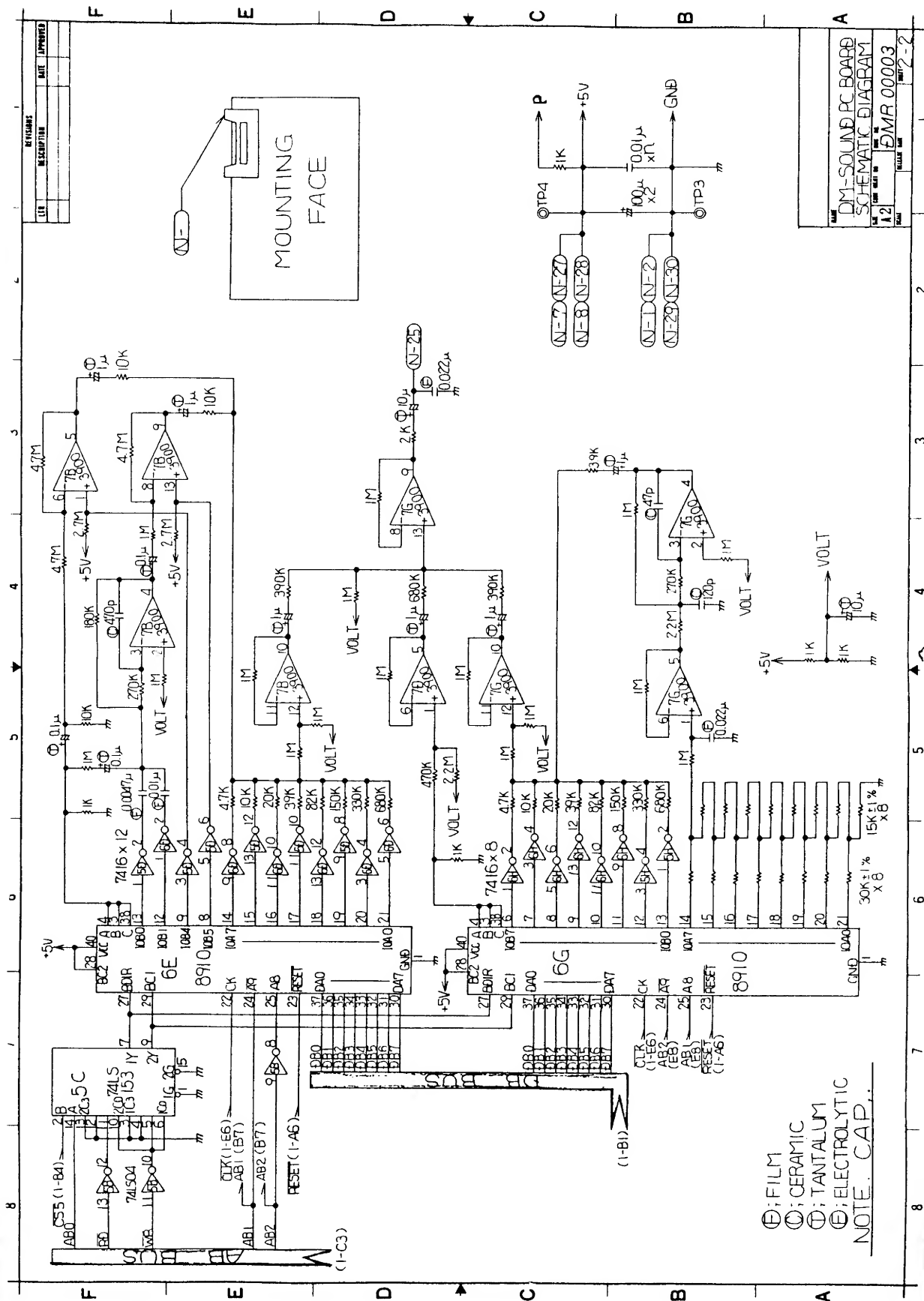
Position Setting		1 WAY (LEFT)				2 WAY (RIGHT)			
		1	2	3	4	5	6	7	8
*	1 Coin - 1 Play	ON	ON	ON	ON	ON	ON	ON	ON
	1 Coin - 2 Plays	OFF				OFF			
	1 Coin - 3 Plays	ON	OFF			ON	OFF		
	1 Coin - 4 Plays	OFF				OFF			
	1 Coin - 5 Plays	ON	ON	OFF		ON	ON	OFF	
	1 Coin - 6 Plays	OFF				OFF			
	1 Coin - 7 Plays	ON	OFF			ON	OFF		
	1 Coin - 8 Plays	OFF				OFF			
	2 Coins - 1 Play	ON	ON	OFF	ON	ON	ON	OFF	
	3 Coins - 1 Play	OFF			OFF				
	4 Coins - 1 Play	ON	OFF		ON	OFF			
	5 Coins - 1 Play	OFF			OFF				
	6 Coins - 1 Play	ON	ON		OFF	ON	ON		OFF
	7 Coins - 1 Play	OFF				OFF			
	8 Coins - 1 Play	ON	OFF			ON	OFF		
	9 Coins - 1 Play	OFF				OFF			

o DIP Switch (C)

Table 3

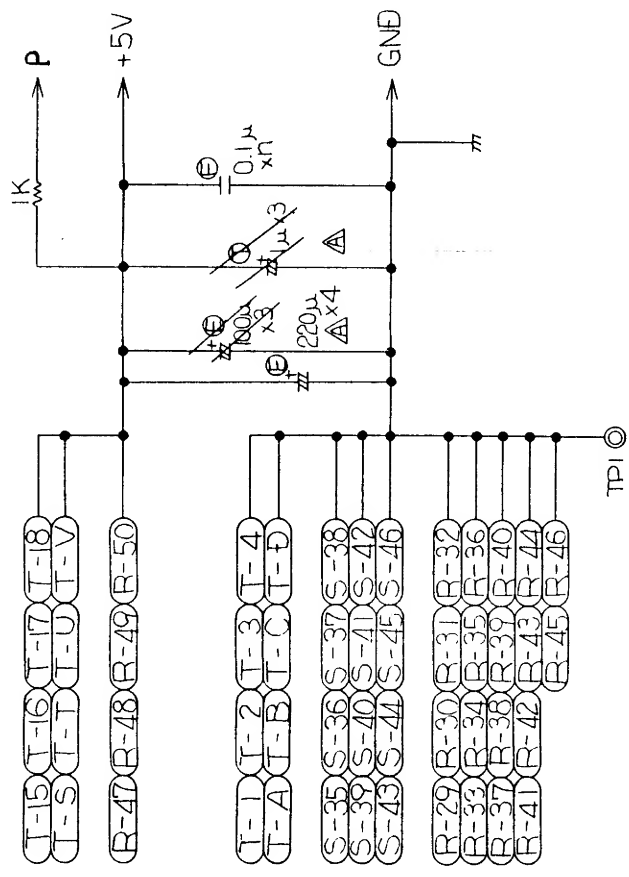
Position Setting		1	2	3	4	5	6	7	8
Appearance of Crow *	Without	ON							
	With	OFF							
The hole from which a crow appears and flies with drawing a circle or a 8-shape in the air.	Circle 1,7,11,15,18 8-shape 3,5,9,13,17		ON	ON					
	Circle 4,7,13,18 8-shape 1,10,16,17		ON	OFF					
	Circle 1,10,16,18 8-shape 4,7,13,17		OFF	ON					
	Circle 3,5,9,13,18 8-shape 1,7,11,15,17		OFF	OFF	OFF				
Coin Display *	No Display					ON			
	Display					OFF			
Year Display *	No Display						ON		
	Display						OFF		
Check *	Check							ON	
	Normal							OFF	
Coin System *	1 Way								ON
	2 Way								OFF

. Factory Adjustment: *



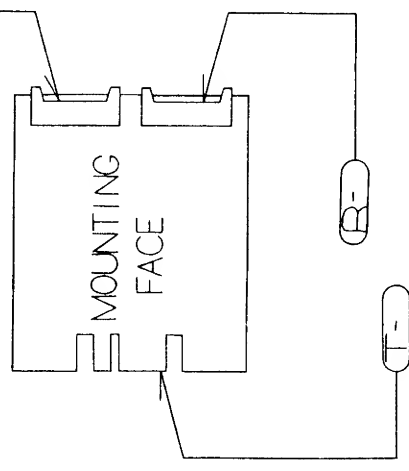
Ⓔ; FILM
Ⓢ; CERAMIC
Ⓣ; TANTALUM
Ⓔ; ELECTROLYTIC
NOTE: CAP.

REVISIONS			
ITER	DESCRIPTION	DATE	APPROVED
1	2 POINT	8-71-15	WJG/CHL



- ⊖ ; ELECTROLYTIC
- ⊕ ; TANTALUM
- ⊖ ; FILM
- ⊙ ; CERAMIC

NOTE. CAP.



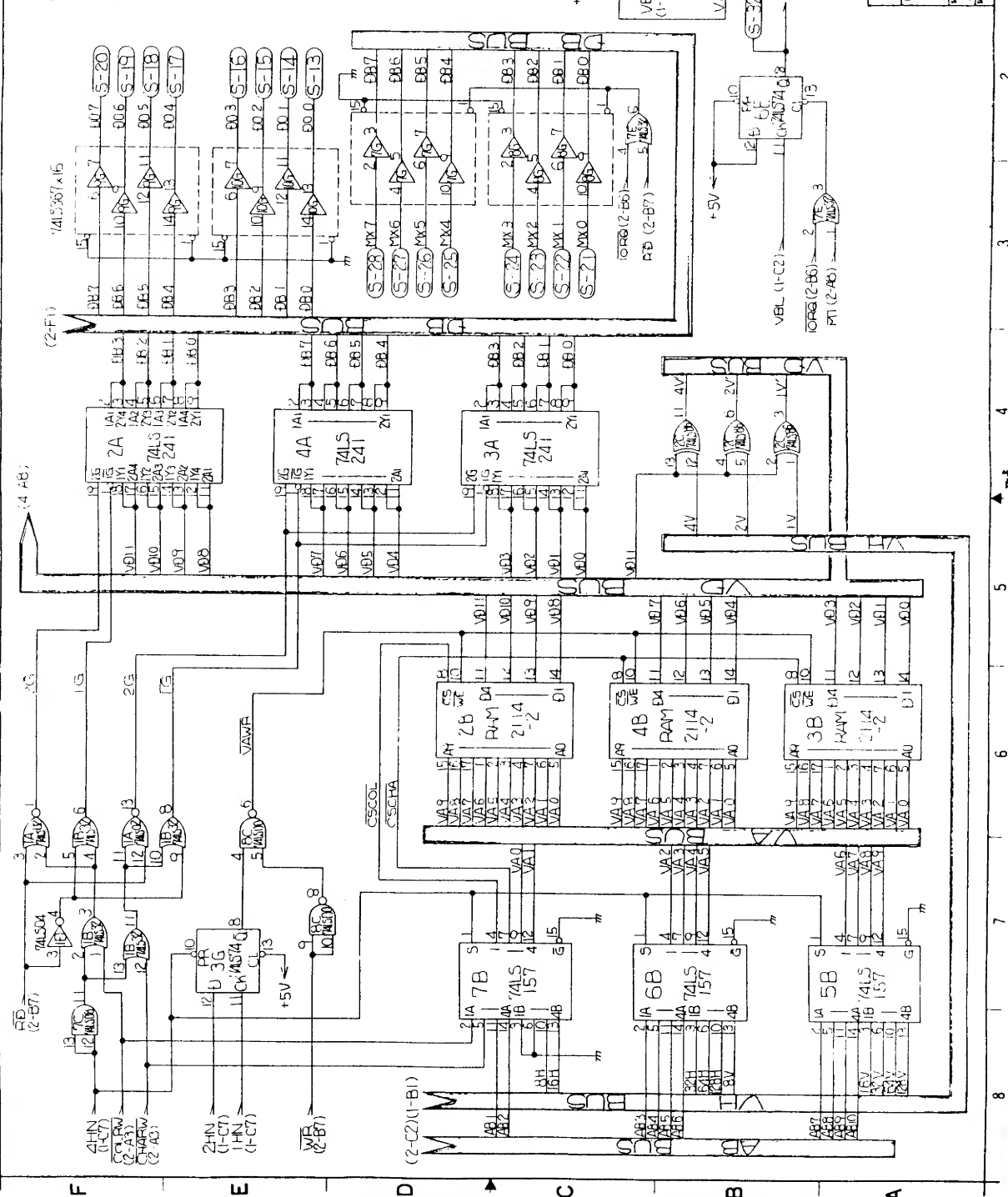
NAME	EM-CPU PC BOARD
SIZE	A 3
CON. UNIT	EMR 00002
SCALE	5-5

A horizontal line with points labeled A, B, C, D, E, and F from right to left. An arrow points to point D.

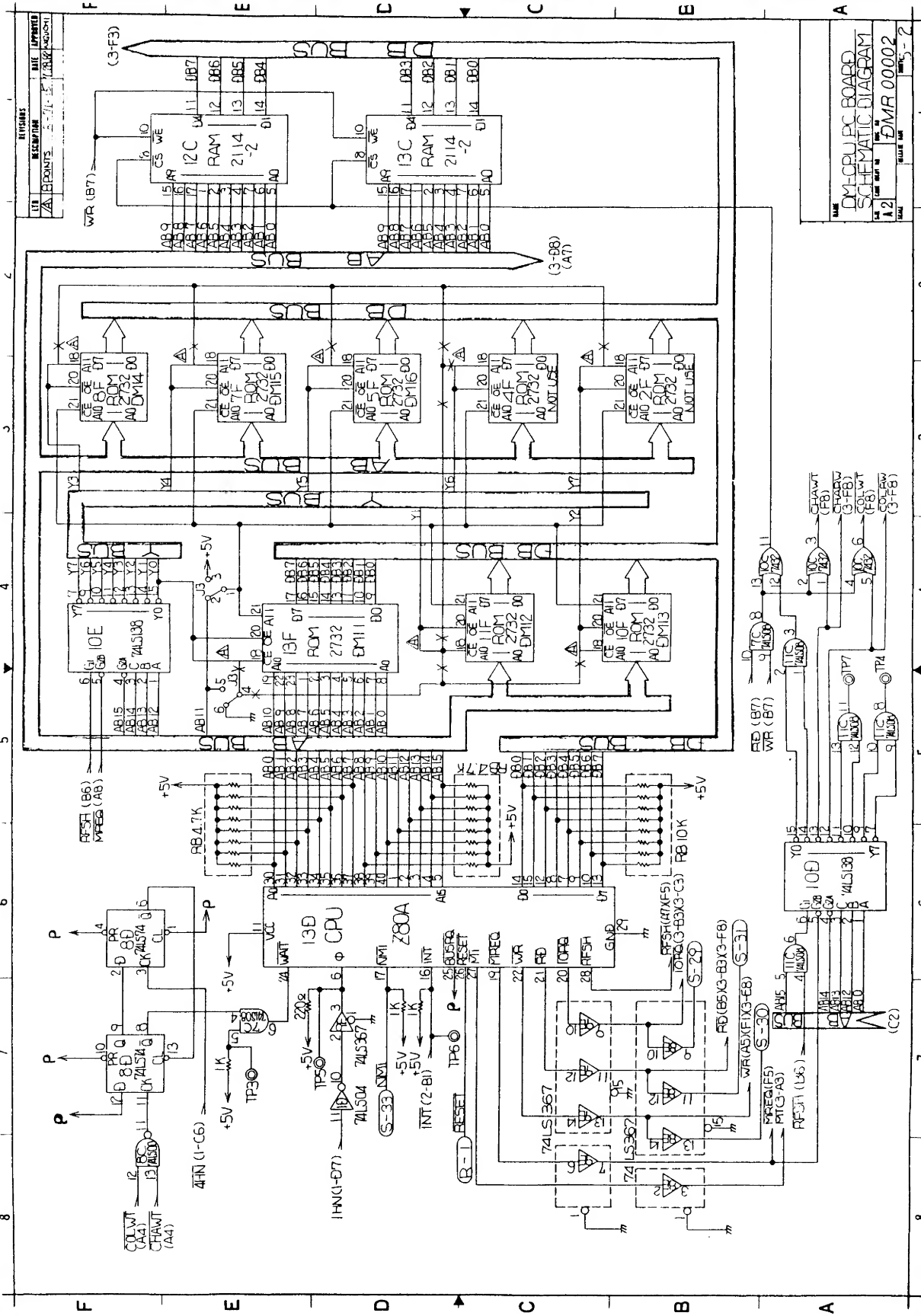


NAME	DM-CPU PC BOARD		
	SCHEMATIC DIAGRAM		
QTY	1	DATE	00002A
REV	1	DATE	00002A

REV	DESCRIPTION	DATE	APPROVED
1	1.0	10/1/80	10/1/80

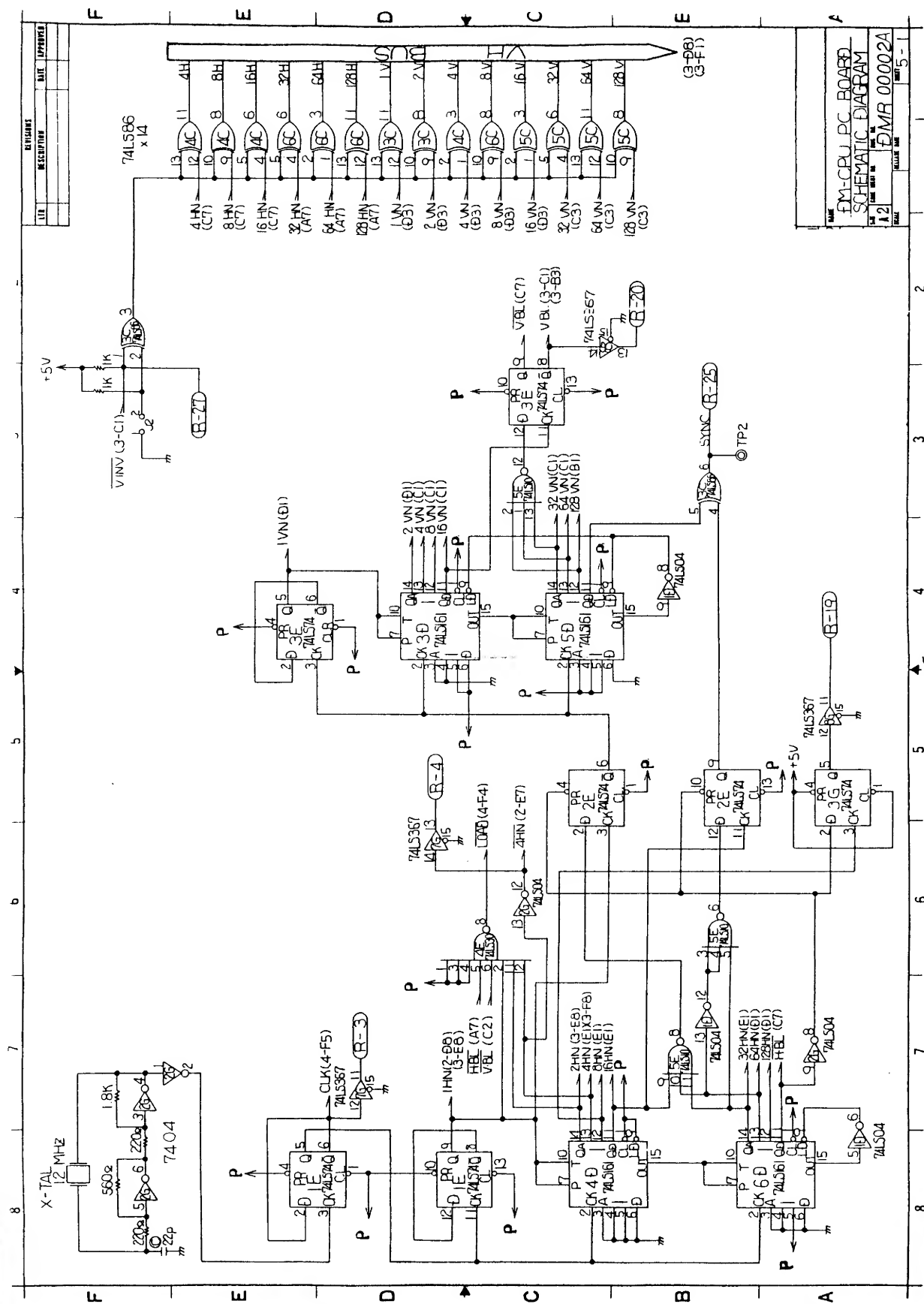


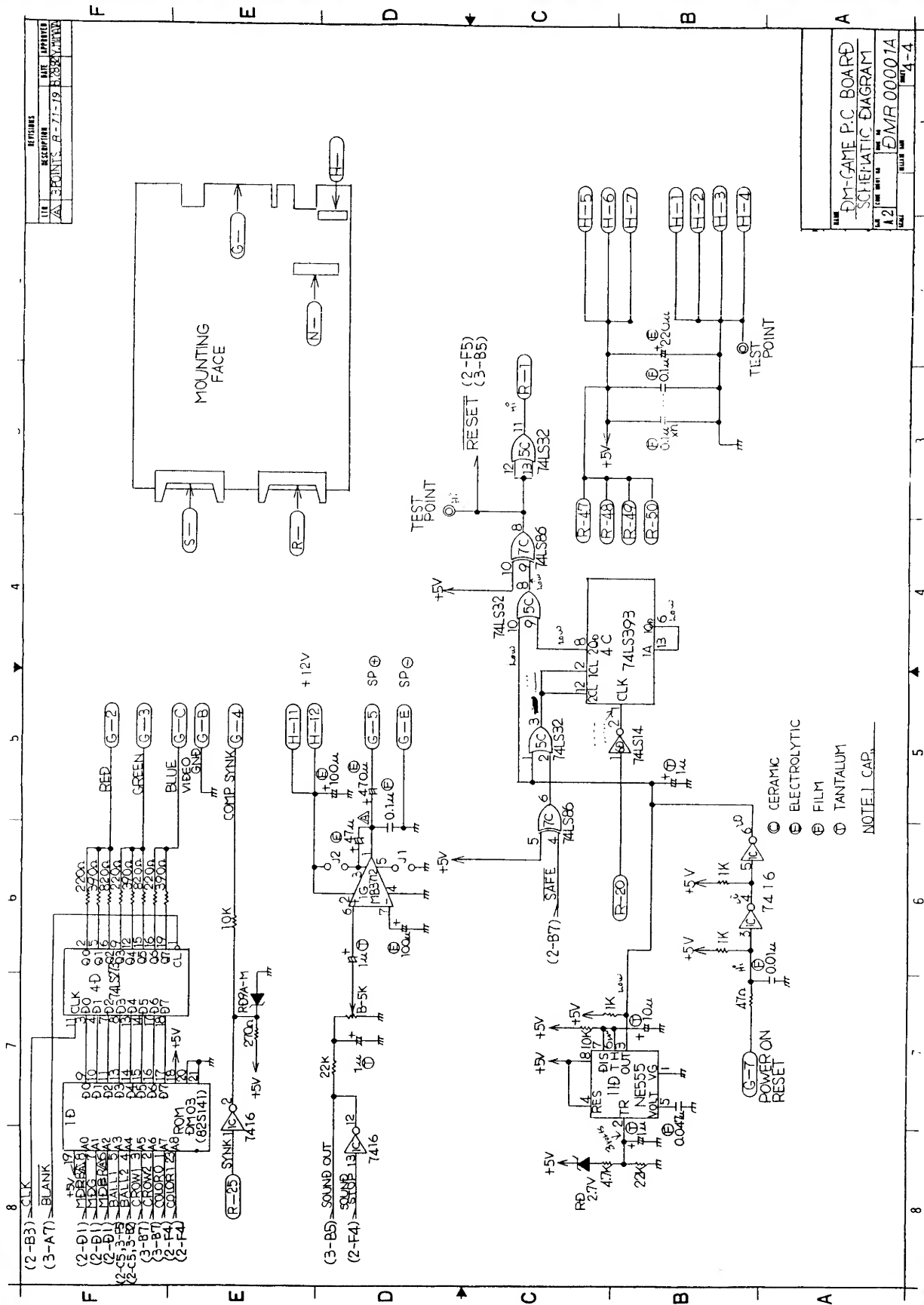
DM-CPU-PC BOARD
SCHEMATIC DIAGRAM
1.2
DMR 00002

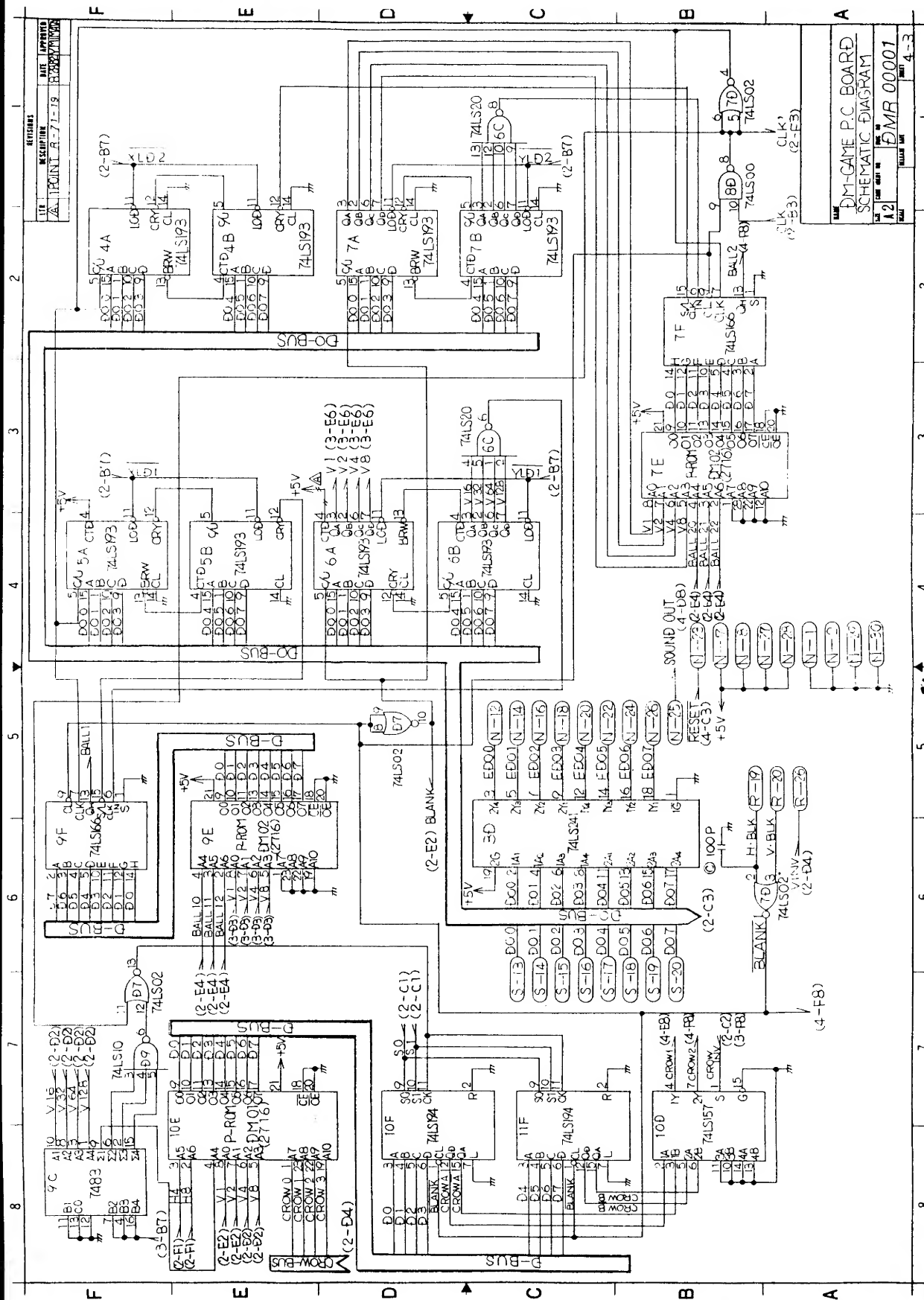


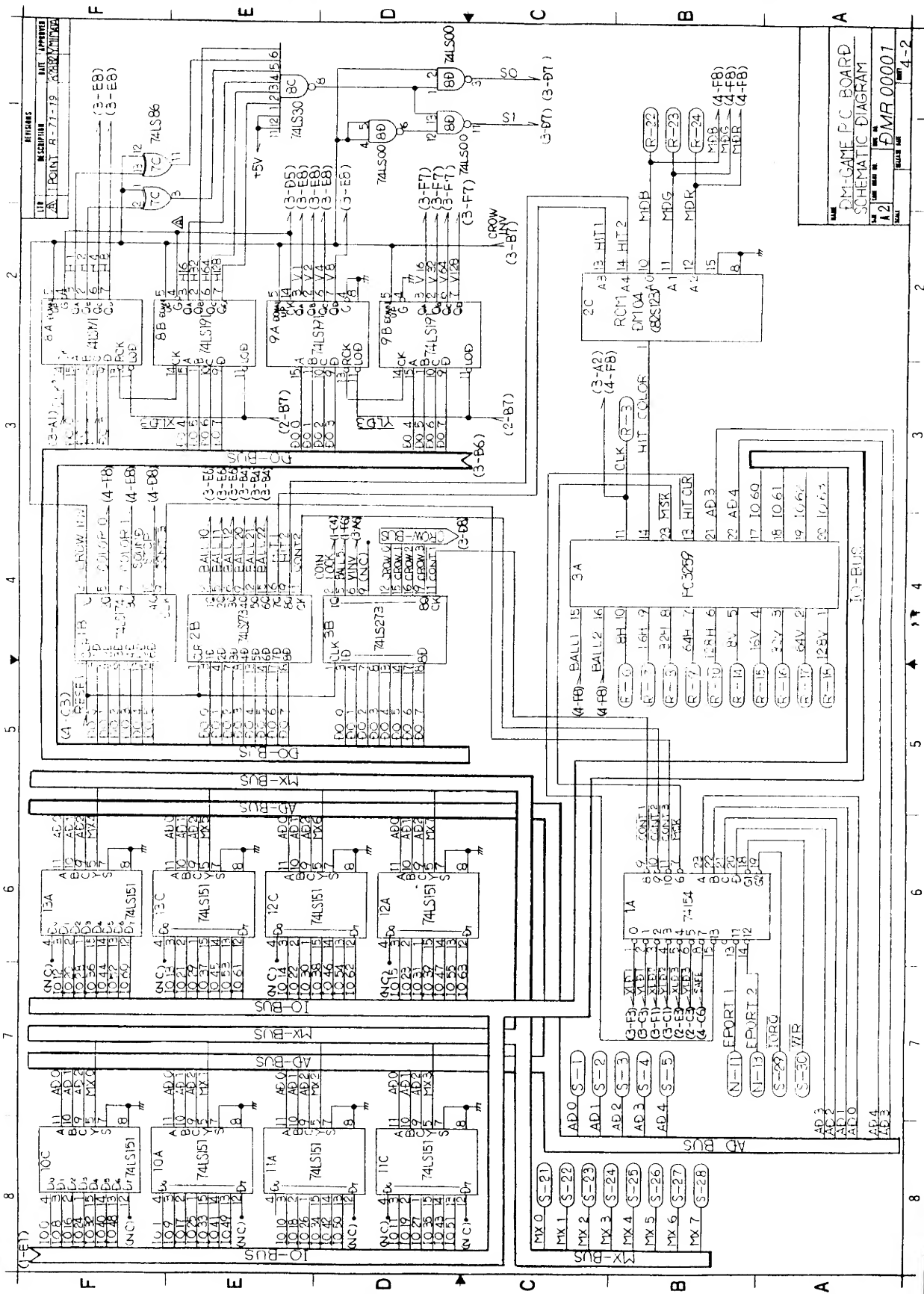
NUCARS
LUA
REVISION
DATE
1/8/80
1/8/80

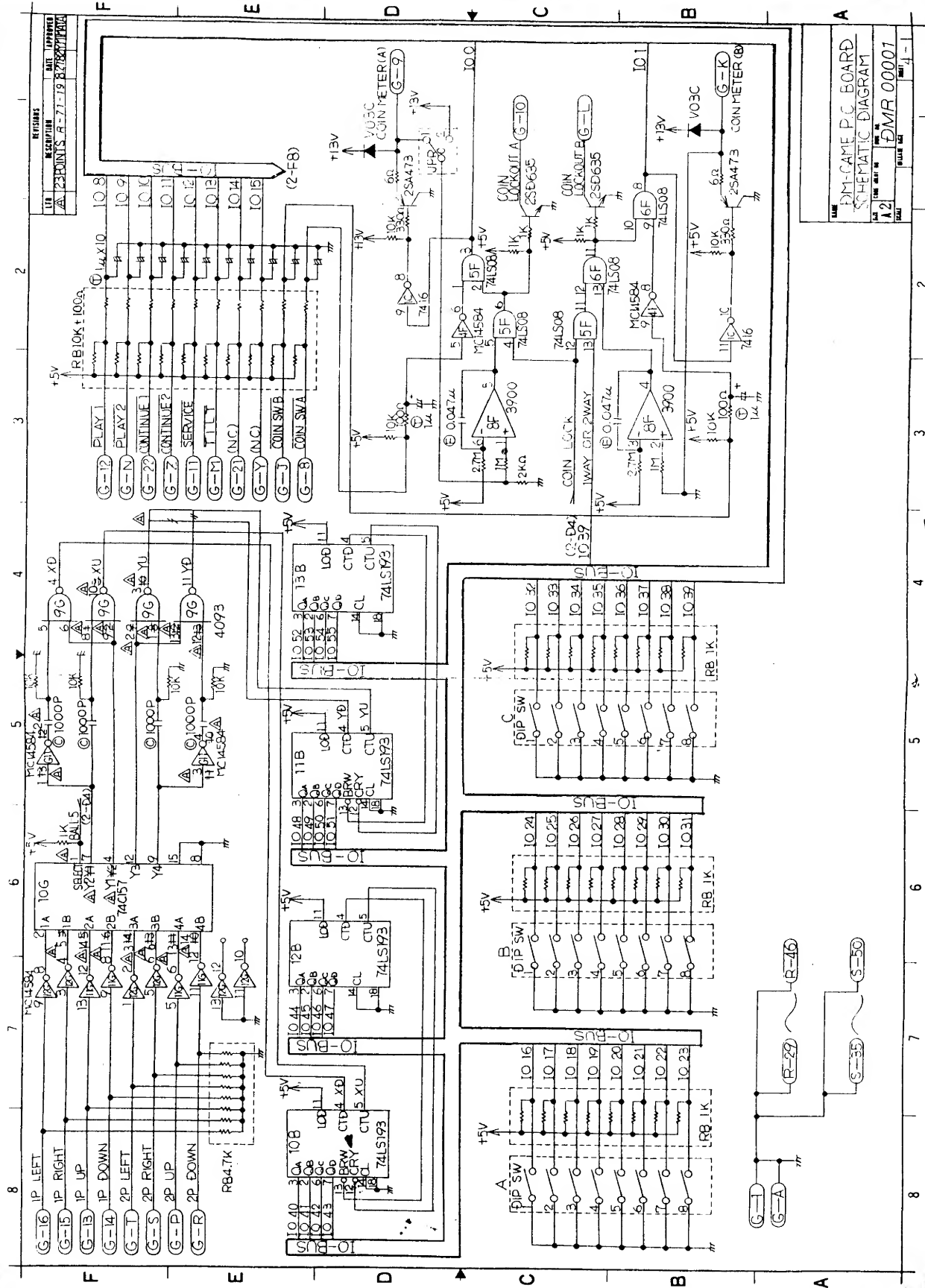
DM-CPU PC BOARD
SCHEMATIC DIAGRAM
1.2
DMR 00002
1/8/80











DM-CAME PC BOARD
SCHEMATIC DIAGRAM
A2
DMR 00001

